

IN THE CLAIMS

Please cancel claims 10-14 without prejudice and amend claims 4, 7 and 15-16 as follows:

1 1. (Original) A transmission system comprising:
2 a random bit generator that is configured to provide an
3 unpredictable sequence of bits,
4 a transmitter that is configured to communicate the
5 unpredictable sequence of bits, and
6 a storage device that is configured to store data
7 corresponding to the unpredictable sequence of bits, to facilitate
8 a verification of receipt of the unpredictable sequence of bits by
9 a receiving system.

1 2. (Original) The transmission system of claim 1, wherein
2 the transmitter includes at least one of:
3 a radio-frequency transmitter,
4 an infrared transmitter, and
5 a sonic transmitter.

1 3. (Original) The transmission system of claim 1, wherein

2 the unpredictable sequence of bits is communicated via at
3 least one of:
4 frequency modulation,
5 on-off keying of a carrier signal,
6 return-to-zero encoding, and
7 a synchronizing pilot signal.

1 4. (Currently Amended) A receiving system comprising

2 a receiver that is configured to receive a modulation

3 | from a transmitter corresponding to a random bit sequence only,

4 a detector that is configured to detect the random bit

5 sequence from the modulation, and

6 a storage device that is configured to store data

7 corresponding to the random bit sequence, to facilitate a

8 | verification of receipt of the random bit sequence from said

9 | transmitter.

1 5. (Original) The receiving system of claim 4, wherein
2 the receiver includes at least one of:
3 a radio-frequency receiver,
4 an infrared receiver, and
5 a sonic receiver.

1 6. (Original) The receiving system of claim 4, wherein

2 the modulation includes at least one of:

3 frequency modulation,

4 on-off keying of a carrier signal,

5 return-to-zero encoding, and

6 a synchronizing pilot signal.

1 7. (Currently Amended) The A receiving system of claim 4,
2 further including comprising:
3 a receiver that is configured to receive a modulation
4 corresponding to a random bit sequence only;
5 a detector that is configured to detect the random bit
6 sequence from the modulation;
7 a storage device that is configured to store data
8 corresponding to the random bit sequence, to facilitate a
9 verification of receipt of the random bit sequence; and
10 a mixer that is configured to combine the random bit
11 sequence with an identifier of the receiving system to form the
12 data corresponding to the random bit sequence.

1 8. (Original) The receiving system of claim 7, wherein

2 the mixer is configured to combine the random bit
3 sequence with the identifier of the receiving system using an
4 irreversible transform.

1 9. (Original) The receiving system of claim 4, wherein
2 the modulation is at a relatively low frequency relative
3 to a carrier frequency of a transmitted signal that is received by
4 the receiver.

Claims 10-14 (Cancelled)

1 15. (Currently Amended) The A verification system of claim 14,
2 further including comprising:
3 a first register that is configured to contain received
4 data;
5 a second register that is configured to contain at least
6 a portion of transmitted data;
7 a comparator, operably coupled to the first register and
8 the second register, that is configured to compare the received
9 data to a select portion of the transmitted data;
10 a shift controller that is configured to shift the
11 portion of transmitted data in the second register to provide a

12 next select portion of the transmitted data for comparison, if the
13 comparator indicates a difference between the received data and the
14 select portion of the transmitted data; and
15 a reward system that is configured to provide a reward
16 based on a length of the received data when the comparator
17 indicates a match between the received data and the portion of the
18 transmitted data.

1 16. (Currently Amended) The verification system of claim 14,
2 further including comprising:
3 a first register that is configured to contain received
4 data;
5 a second register that is configured to contain at least
6 a portion of transmitted data;
7 a comparator, operably coupled to the first register and
8 the second register, that is configured to compare the received
9 data to a select portion of the transmitted data;
10 a shift controller that is configured to shift the
11 portion of transmitted data in the second register to provide a
12 next select portion of the transmitted data for comparison, if the
13 comparator indicates a difference between the received data and the
14 select portion of the transmitted data; and

15 a mixer, operably coupled to the second register and the
16 comparator, that is configured to combine at least a part of the
17 portion of the transmitted data with an identifier of a receiver to
18 produce the select portion of the transmitted data that is compared
19 to the received data.

1 17. (Original) A method of providing a reward for being at a
2 particular locale, comprising:

3 transmitting an unpredictable sequence of bits from a
4 transmitter at the particular locale,

5 storing transmission data corresponding to the
6 unpredictable sequence of bits,

7 receiving at least a portion of the unpredictable
8 sequence of bits at a receiver when the receiving system is within
9 receiving range of the transmitter,

10 storing reception data corresponding to the portion of
11 the unpredictable sequence of bits at the receiving system,

12 subsequently comparing the reception data to the
13 transmission data to identify a period of correspondence between

14 the reception data and the transmission data, and

15 determining the reward based on the period of
16 correspondence.

1 18. (Original) The method of claim 17, wherein
2 comparing the reception data to the transmission data
3 includes:
4 partitioning the reception data into a plurality of
5 subsets of reception data, and
6 comparing each subset of the plurality of subsets of
7 reception data to a corresponding subset of the transmission data.